



**NATIONAL INSTITUTE OF TECHNOLOGY ROURKELA**  
**SPONSORED RESEARCH, INDUSTRIAL CONSULTANCY & CONTINUING EDUCATION**

**NITR/SR/2021/Advt-21EE018/L/015(Re-advt.) dt. 06.10.2021**

Applications are invited on prescribed format (On-line) for the temporary post of "Junior Research Fellow (JRF)" for the Project entitled "Development and Prototyping of Multiport Hybrid Power Electronic Converters with Advanced Control for Household Nanogrid." Selected candidate may enroll in Ph.D. Programme in the **Department of Electrical Engineering, National Institute of Technology Rourkela** based on Institute norms and regulations.

1.	Project Code	<b>SR/21/EE/018</b>
2.	Name of the Temporary Post	Junior Research Fellow – 01 (One)
3.	Name of the Research Project	"Development and Prototyping of Multiport Hybrid Power Electronic Converters with Advanced Control for Household Nanogrid"
4.	Name of the Sponsoring Agency	Department of Science and Technology (DST), New Delhi
5.	Tenure of the Project	Three (03) years (from 2021 to 2024)
6.	Tenure of the Assignment	Initial for a period of one year (Extendable to three years upon satisfactory performance)
7.	Job Description	A. To carry out the research work ( <b>Experimental</b> ), in the field of Power Electronics Converters and Controllers Design ( <b>Microcontroller/DSP</b> ) & to visit different industry/ institute (in India) and DST, New Delhi for progress meeting. B. Assist the PI in preparation of reports related to the research work C. Other works related to the project <b>** Scope to enroll as a Ph. D. Scholar</b> in Department of Electrical Engineering (has to satisfy Institute / Department Eligibility Criteria)
8.	Consolidated monthly compensation / Fellowship	INR 31,000 (1st & 2nd year) and INR 35,000 (3rd year) [for NET / GATE]
9.	Essential Qualifications and Experience	M. Tech. or M.E. in Electrical Engineering with 65% marks or 7.0 CGPA along with Institute norms. It is desirable that the student should have handsome experience to work in hardware setup fabrication and controller design in microcontroller or DSP platform.
10.	Desirable Qualifications / Experiences	Valid NET / GATE Score, Simulation/ Experimental skills in designing of different power electronics converter topologies and their control schemes.
11.	Accommodation	Bachelor / Married accommodation in the Institute may be provided subject to availability.

For technical information on the project, the candidate may contact the Principal Investigator at the following address:

Name : Prof. Arnab Ghosh  
Address : Department of Electrical Engineering  
National Institute of Technology Rourkela, Odisha 769 008.  
Tel. No. : 0661-246 2417  
Mobile : 9433379717  
E-mail : [ghosha@nitrkl.ac.in](mailto:ghosha@nitrkl.ac.in) or [agosh.ee@gmail.com](mailto:agosh.ee@gmail.com)

Date and Time for **On-line / Walk-in** Interview: **Will be notified later.**

Place of Interview: **Department of Electrical Engineering, NIT Rourkela**

Eligible candidates may apply within **24.10.2021, 23:59 HRS**. The candidates are required to send the complete filled up application form (Soft copy) to [ghosha@nitrkl.ac.in](mailto:ghosha@nitrkl.ac.in). The application form is available in the following link: [http://nitrkl.ac.in/oldwebsite/ Jobs\\_Tenders/5ProjectFellowships/Doc/JRF%20LS-PND-64\(2\).pdf](http://nitrkl.ac.in/oldwebsite/Jobs_Tenders/5ProjectFellowships/Doc/JRF%20LS-PND-64(2).pdf) which must be filled by candidates and also required to attach photocopies of all supporting documents, research papers (if any) etc.

Candidates will build a **Single PDF** file; it is to be sent to Principal Investigator ([ghosha@nitrkl.ac.in](mailto:ghosha@nitrkl.ac.in)) **on or before 24.09.2021, 23:59 HRS**. The candidates are also required to produce relevant documents mentioned in the application form [such as original of all mark sheets and certificates, research papers (if any), experience certificate (if any) etc.] at the time of interview / joining. Selection / Joining will be canceled in case of any suppression of information / document provided earlier.

Mere possession of minimum qualification does not guarantee invitation to the interview. Candidates will be short listed based on merit and need of the project.

**Sd/-  
Asst. Registrar (SR)**

Copy to:

- 1) All Heads of the Departments, NIT Rourkela for publication on departmental notice boards.
- 2) Prof. Arnab Ghosh, PI with a request to give wide publicity to this advertisement.
- 3) Head of the Department, Electrical Engineering (EE).
- 4) Project file.