

INDIAN INSTITUTE OF TECHNOLOGY, ROORKEE
(Electronics and Communication Engineering)

Dated... 26/7/21

ADVERTISEMENT TO FILL UP PROJECT POSITIONS*

Applications are invited from Indian nationals only for project position(s) as per the details given below for the consultancy/research project(s) under the Principal investigator (Name: Prof. Anand Bulusu), Dept. Electronics and Communication Engineering, Indian Institute of Technology, Roorkee.

1. Title of project: **Development and Efficient Characterization of Floating Body (FB) and Dynamic Threshold (DT) CMOS Partially Depleted Silicon-on-Insulator (PDSOI) Standard Cell Libraries**
2. Sponsor of the project: **DST, New Delhi**
3. Project position(s) and number: **SRF (01)** *with Grade.*
4. Qualifications: **M.Tech (VLSI) or BTech (Electronics/Electronics and Communication) with preferably 1 year of work experience with the knowledge of embedded systems/VLSI/Verilog/VHDL/C/C++.**
Knowledge of EDA Tools
5. Emoluments: **Rs. 35000 per month + H.R.A (as per rules)**
6. Duration: **till 14/04/2022**
7. Job description: **Generating Verilog description, abstract view, and timing library files for standard cell**
8. *The Selected SRF may have to stay in IIT Guwahati and for SCL, Chandigarh, for a major part of the project.*
1. Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply.
2. Candidates desiring to appear for the Interview should submit their applications with the following documents to the office of Principal Investigator through email, by post or produce at the time of Interview:
 - Application in a plain paper with detailed CV including chronological discipline of degree/certificates obtained.
 - Experience including research, industrial field and others.
 - Attested copies of degree/certificate and experience certificate.
3. Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of interview for verification.
4. Preference will be given to SC/ST candidates on equal qualifications and experience.
5. Please note that no TA/DA is admissible for attending the interview.

The last date for application to be submitted to office of Principal Investigator is 05/8/2021 by 5 PM.
(not applicable for walk in interview)

The interview will be held at on at (to be given only for walk in interview)

Tel:

Fax:

Email: anand.bulusu@ece.iitr.ac.in

Approved
Manish
26/07/2021
Subham
26/07/2021
for
26/7/21
B. Anand
Name and signature
of Principal Investigator

*To be uploaded on IIT Roorkee website and copy may be sent to appropriate addresses by PI for wider circulation.