

SRIC/06

INDIAN INSTITUTE OF TECHNOLOGY, ROORKEE
(Name of Dept./Centre)

Dated: 19/07/2021


ADVERTISEMENT TO FILL UP PROJECT POSITIONS*

Applications are invited from Indian nationals only for project position(s) as per the details given below for the research project under the Principal investigator (Name: **Dr. Biswarup Das**), **Department of Electrical Engineering**, Indian Institute of Technology, Roorkee.

1. Title of project: **Development of Indigenous Simulation Model for Design Validation of Power Supply of High Speed railways**
2. Sponsor of the project: **NHSRCL, New Delhi**
3. Project position(s) and number: **02 JRF**
4. Qualifications: **M. Tech./M. E. Electrical (Specialization in Power System)**
5. Emoluments: **31,000 + HRA (as per institute rules)**
6. Duration: **Three years or till the end of the project**
7. Job description:
 - **Development of software program for time variant power flow analysis, harmonic power flow analysis, short circuit analysis, calculation of induced voltage and calculation of temperature rise in OHE of electric traction network.**
 - **Validation of the developed software program by comparing its output with the results i) obtained with other simulation platform, ii) available in existing reports and iii) obtained with the field test and measurement process (if available).**
 - **Compiling the results, data interpretation, writing research paper and project reports.**

Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply.

8. Candidates should submit their applications with the following documents to the office the principal investigator through e-mail (biswarup.das@ee.iitr.ac.in) by **August 15, 2021**:
 - Application in a plain paper with detailed CV including chronological discipline of degree/certificates obtained.
 - Experience including research, industrial field, and others.
 - Self-attested copies of degree/certificate and experience certificate.
 - Proof of qualifying GATE score (including marks obtained, rank and year of qualification)
9. Candidates shall bring along with them the original degree(s)/certificate(s), experience certificate(s) and proof of qualifying GATE score at the time of interview for verification.
10. Preference will be given to SC/ST candidates on equal qualifications and experience.
11. Please note that no TA/DA is admissible for attending the interview.
12. On-line interview would be conducted for outstation candidates on request.

 19/07/2021

Dr. Biswarup Das
Principal Investigator

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*To be uploaded on IIT Roorkee website and copy may be sent to appropriate addresses by PI for wider circulation.