



भारतीय प्रौद्योगिकी संस्थान गोवा  
INDIAN INSTITUTE OF TECHNOLOGY GOA  
OFFICE OF THE DoRD

No. DoRD/ADMIN/2021-22/011

Date: 26.07.2021

**RECRUITMENT FOR JUNIOR RESEARCH FELLOW**

Applications are invited for the position of JRF under the Project titled “**Development and Implementation of Different Robust Control Algorithms for Plasma position control in a Tokamak**”, in the School of Electrical Sciences, IIT Goa. The project is sponsored by Board of Research in Nuclear Sciences (BRNS), Department of Atomic Energy (DAE), Government of India.

<b>Nature of work</b>	Implementation of Control Algorithms in MATLAB/SIMULINK and in FPGA Research Discussion with Scientists.
<b>Funding Agency</b>	Board of Research in Nuclear Sciences
<b>Sanction No.</b>	57/14/03/2021-BRNS/57027
<b>No. of Position</b>	<b>One</b>
<b>Duration of appointment</b>	Initially for a period of 1 year. Appointment may be extended for further one year based on the performance.
<b>Eligibility</b>	<b>Essential qualification:</b> - M. E/M. Tech in Electrical Engineering/Electronic & Communication/Control System Engineering/ Instrumentation/Embedded System -B.E. or B. Tech in Electrical Engineering/Electronics & Communication Engineering /Instrumentation & Control - Must have a valid GATE. <b>Desirable:</b> -Exposure in FPGA, VHDL, Python /C/MATLAB
<b>Salary</b>	Rs.31,000/- P.M.(As per BRNS norms)
<b>DA</b>	Not be entitled to DA.
<b>HRA</b>	As per the Govt. norms stationed at Goa.
<b>Medical Benefits</b>	As per the Institute norms.
<b>Leave entitlements</b>	Casual leaves of 8 days per annum are available. If the JRF joins during the mid-year, CL would be calculated as per pro-rata basis.
<b>How to apply</b>	Interested candidates can fill the below form. <a href="https://docs.google.com/forms/d/e/1FAIpQLSe49RFuv7Y7x6nDbeM4_CZ3e75k6u2npbVND_tL6tG3fUNnEq/viewform?usp=sf_link">https://docs.google.com/forms/d/e/1FAIpQLSe49RFuv7Y7x6nDbeM4_CZ3e75k6u2npbVND_tL6tG3fUNnEq/viewform?usp=sf_link</a>
<b>Last Date of application</b>	15.08.2021,5:00 PM.

**Additional Instructions:**

1. If the number of applications received in response to advertisement is large. The constituted selection committee may restrict the number of candidates to be called for interview to a reasonable limit of desirable qualification and /or on the record of academic performance and/or any other benchmark decided by a committee constituted to screen the applications. No communication will be entertained with candidates who are not called for Interview.
2. If the applicants have any questions, they can write to [res-staff-rect@iitgoa.ac.in](mailto:res-staff-rect@iitgoa.ac.in) and in the subject they have to mention the Institute project number “**2021/EMR/BS/025**”.
3. Informal inquiries via email may be made with PI- Prof. Bidyadhar Subudhi, School of Electrical Sciences: [bidyadhar@iitgoa.ac.in](mailto:bidyadhar@iitgoa.ac.in)
4. The appointment is purely temporary and will terminate automatically without any notice or compensation on termination of the research project.
5. The appointed person shall have no claim of appointment / absorption in funding Agency or in IIT Goa.

6. The qualification prescribed should have been obtained from recognized Universities / Institutions.
7. No TA/DA will be admissible for appearing for the interview.
8. Selected candidates will have to join duty immediately on receipt of the offer.



**Principal Investigator (PI)**