Indian Institute of Technology (ISM), Dhanbad, India

The Office of Dean, Research & Development

Dhanbad, India – 826004

Advertisement for the Post of JRF

Sponsored by Department of Science and Technology (**DST**), **Interdisciplinary Cyber Physical Systems (ICPS) / CPS** Division, Govt. of India

Sanction No. **DST/ICPS/CPS-Individual/2018/973(G)**	IIT(ISM) Project No.: DST(212)/2018-2019/619/ECE	Date: 20-07-2021

Applications are invited for the position of **JRF** (Junior Research Fellow) (one post) in DST sponsored project. The details are given below:

Name of the Position	**JRF** (Junior Research Fellow)
Number of Position(s)	One (01)
Tile of The Project	Design and efficient implementation of low cost and power proficient model of Cyber Security System Physical Layer Circuit through accomplished powerful algorithm by advanced innovative electronic active devices.
Principal Investigator	**Dr. Kaushik Mazumdar** Department of Electronics Engineering Indian Institute of Technology (Indian School of Mines) Dhanbad -826004, Jharkhand Email: **kaushik@iitism.ac.in** Phone no.:- (0326)2235937(O), +91-9771420587(M)
Tenure of Project	The position is purely temporary and co-terminus with the project, which was sanctioned for a maximum period of **36 MONTHS** and would be continued depending upon the **satisfactory performance** of the JRF advertised here, **as decided by the PI of the project.**
Job Description	Design, development, security of IoT, cloud system. The project JRF assist in developing various techniques to overcome security issues in IoT architecture and communication engineering.
Essential Qualification	M.E. / M.Tech. in Computer Science & Engineering / Software Engineering / Information Technology / Cyber Security / Information Security / Electronics and Communication Engineering / VLSI Design / Communication and Signal Processing / Electronics and Instrumentation Engineering / Telecommunication Engineering / Communication Engineering, having minimum 7 CGPA or 7.5 DGPA / 65% marks **WITH** B.E. / B.Tech. in Computer Science & Engineering / Information Technology / Electronics and Communication Engineering / Electronics and Instrumentation Engineering / Electrical Engineering / Instrumentation and control Engineering or MCA / M.Sc in Electronics and Communications with minimum 7 CGPA or 65% marks from reputed institutions / university. In case class is not specified in qualification then minimum 60% marks or 7.0 CGPA will be considered as 1st class.
Desirable Qualification	Working knowledge of Design and Analysis of Cyber Security System using efficient algorithm. The project fellow need to apply various programming languages to implement various techniques for IoT architecture and should have basic knowledge for IoT implementations etc. **GATE/NET Qualified with a valid score.**
Age and Relaxation	The upper age limit is 28 years at the time of appointment (Age relaxation for SC/ST/OBC/PH/Female candidate as per GOI rule).
Fellowship	**Rs. 25,000/-** @ pm. for two years and **Rs. 28,000**/- for 3rd year as SRF**. (Requested DST to may increase it to as per latest norms 31,000/- pm & 35,000/- pm.)**
Last Date and Time for Online submission of application	**August 20, 2021 5:30 PM** Online Application link: https://forms.gle/3nDzRY2vucbDZCUm8
Application Procedure	Interested candidate are requested to fill the online application (as per the link provided) and must attach all supporting documents (e.g. self-attested copies of educational qualification, experience certificate, age proof, valid caste certificate (if applicable), copies of publication, awards, recommendation letter, CV with photo, GATE/NET certificate, etc.)

Note:

Shortlisted candidate will be informed through email about the interview. The interview may be on online mode (google meet/Zoom). The time along with the online details would be sent to the shortlisted candidate by email. Mere possession of the minimum qualifications does not guarantee an invitation to the interview. Candidate will be shortlisted based on their merits and as per the requirements of the project. No TA/DA will be paid to attend the interview.

Dr. Kaushik Mazumdar

(Principal Investigator)

Project No.: DST (212)/2018-2019/619/ECE & Sanction No. **DST/ICPS/CPS-Individual/2018/973(G)**

Title of the project: Design and efficient implementation of low cost and power proficient model of Cyber Security System Physical Layer Circuit through accomplished powerful algorithm by advanced innovative electronic active devices.

Application for the Post of Junior Research Fellow(JRF)

Affix passport size photograph.

1. Name (in block letters):_____

2. Date of Birth (DD/MM/YY):_____

3. Address for Communication:

_____

_____

_____

City:_____Pin :_____

Phone No.:_____

*Email:_____

Permanent Address:

_____

_____

_____

City:_____Pin :_____

Phone No.:_____

Please make sure that the e-mail ID is correct and legible.

4. Details of University/ Institution Studied (10th onwards):

Degree / Examination Passed	Subjects/Area of specilaization	Institution	Year of Passing	Marks in % or DGPA/ CGPA	Class

5. Additional Qualifying Examination(s) Passed (if any):

(a) CSIR/UGC- NET-JRF

Registration/Roll No.:_____

Date of exam:_____

Valid till:_____

(b) GATE

Registration/Roll No.:_____

Score (percentile and Rank):_____

Valid till:_____

6. Experience (Teaching/Research/Industrial etc) if any:

Name of the Organization	Designation	Period		Nature of Work
		From	To	

7. Research Publications (if any):

8. Personal Information:

a	Nationality	
b	Gender	
c	Marital status	
d	Father's/Spouse's Name	
e	Whether person with disability : Yes/No (if yes, furnish necessary document)	
f	Whether belongs to reserved category : SC/ST/OBC	

9. Address of PI:

Prof. Kaushik Mazumdar (Principal Investigator)
Department of Electronics Engineering
Indian Institute of Technology (Indian School of Mines)
Dhanbad -826004, Jharkhand
Email: kaushik@iitism.ac.in
Phone no.: - (0326)2235937(O), +91-9771420587(M)

Send the completed application (in PDF format) at https://forms.gle/3nDzRY2vucbDZCUm8 [Fill, scan (clearly visible with all the points as per the provided Serial numbers), attach and upload (as single combined file) with all the mentioned necessary academic documents (in pdf format only)]

Note : Incomplete applications will not be considered.

DECLARATION

I hereby declare that I have carefully read the instructions and particulars supplied to me and that the entries made in this application form are correct to the best of my knowledge and belief.

Place: **Signature of the Applicant**
Date: