

इंडियन इंस्टीट्यूट ऑफ टेक्नोलॉजी दिल्ली
हौज खास, नई दिल्ली -110016
(औद्योगिक अनुसंधान एवं विकास इकाई)
INDIAN INSTITUTE OF TECHNOLOGY DELHI
Hauz Khas, New Delhi-110016
(Industrial Research & Development Unit)

No. IITD/IRD/RP04121G/ 338684

Dated:23 /07/2021

Advertisement No.: IITD/IRD/120/2021

Applications from Indian nationals are invited for Project Appointment under the following project. Appointment shall be on contractual basis with consolidated pay, renewable yearly or upto the duration of the project, whichever is earlier. निम्नलिखित परियोजना के तहत भारतीय नागरिकों से आवेदन आमंत्रित किए जाते हैं। अपॉइंटमेंट, अनुबंधित आधार पर समेकित वेतन, नवीकरणीय वार्षिक या परियोजना की अवधि तक, जो भी पहले हो, के साथ होगा.


Title of the Project	Portable and High Precision Compact Gravimeter for Field Applications (RP04121G)	
Funding Agency	Office of the Principal Scientific Adviser	
Name of the Project Investigator	Prof. Bodhaditya Santra	
Deptt./Centre	Department of Physics	
Duration of the Project	Upto:07/06/2026	
Post (s)	Consolidated Pay-slab / Fellowship	Qualifications
Project Scientist 'D' (1)	Rs.78,000/-p.m. plus HRA @ 24%. Increment of 5% for every 2 years of experience subject to performance review. Age limit: 45 years	(1) 1 st class M.Sc./B.Tech. in Physics/ Electrical Engineering/ Electronics and communication engineering/ Computer Engineering or related disciplines with 7 years' experience in R&D (2) Ph.D. preferred (3) 2 years or more experience in developing and debugging industry-standard embedded systems with FPGA for hardware interfacing and data acquisition (4) Strong Python programming skills (5) PCB design and debugging (6) Digital design, wireless communication and automation. (7) Excellent communication skills.
OR Project Scientist 'C' (1)	OR Rs.67,000/- p.m. plus HRA @ 24%. Increment of 5% for every 2 years of experience subject to performance review. Age limit:40 years	OR (1) 1 st class M.Sc./B.Tech. in Physics/ Electrical Engineering/ Electronics and communication engineering/ Computer Engineering or related disciplines with 3 years' experience in R&D (2) Ph.D. preferred (3) 2 years or more experience in developing and debugging industry-standard embedded systems with FPGA for hardware interfacing and data acquisition (4) Strong Python programming skills (5) PCB design and debugging (6) Digital design, wireless communication and automation. (7) Excellent communication skills.

The post of Project Scientist 'D' may be downgraded to Project Scientist 'C' as per discretion of the Selection Committee if none of the candidate is found suitable for the post Project Scientist 'D'.

The candidates who are interested to apply for the above post should download Form No. IRD/REC-4 from the IRD Website (<http://ird.iitd.ac.in/rec>) of IIT Delhi and submit the duly filled form with complete information regarding educational qualifications indicating percentage of marks/division, details of work experience etc. along-with a copy of detailed resume by e-mail with advertisement No. on the subject line to Prof. Bodhaditya Santra at his email ID: bsantra@physics.iitd.ac.in

IIT Delhi reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post and their name will be displayed on web link (<http://ird.iitd.ac.in/shortlisted>) alongwith the online interview details. Only short-listed candidates will be informed for online interview. In case any clarification is required on eligibility regarding the above post, the candidate may contact Prof. Bodhaditya Santra at email id: bsantra@physics.iitd.ac.in

5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms. **The last date for submitting the completed applications by e-mail is 09/08/2021 by 5.00 p.m.** The shortlisted candidates who are called for interview should bring original certificates (both professional and academic) with a recent passport size photograph at the time of interview.


सहायक कुलसचिव, आईआरडी

वितरण

- Head of the Deptt./Centres/Units : It is requested that the contents of the Above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit
- Webmaster, IRD : To put advertisement at IITD website.
- Notice Boards
- Advertisement file
- Prof. Bodhaditya Santra, PI, Department of Physics
- Copy to Chairperson, DRC/CRC
- Dr. Harshita Bhatnagar, RD Coordinator, (R&D) Wing

